## Refine Search

#### Search Results -

| Terms   | Documents |
|---|-----------|
| (plurality or multiple or more than one or various) adj3 graphics adj2 (engine or   | 288       |
| accelerator or block or unit or section or module or logic or component or element) |           |

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

|        |       |       | Refine Search |
|--------|-------|-------|---------------|
| Recall | ext 🗲 | Clear | Internet      |

#### Search History

### DATE: Wednesday, January 26, 2005 Printable Copy Create Case

| Set Name side by side | Query  | <u>Hit</u><br>Count | Set<br>Name<br>result<br>set |
|-----------------------|--|---------------------|------------------------------|
| DB =                  | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ   |                     |                              |
| <u>L19</u>            | (plurality or multiple or more than one or various) adj3 graphics adj2 (engine or accelerator or block or unit or section or module or logic or component or element)                        | 288                 | <u>L19</u>                   |
| <u>L18</u>            | L16 same (swith\$3 or dispach\$3r or schedul\$3 or distribut\$3 or manag\$3 or control\$4)   | 31                  | <u>L18</u>                   |
| <u>L17</u>            | L16 same (swith\$3 or dispach\$3r or schedul\$3 or distribut\$3 or manag\$3)   | 7                   | <u>L17</u>                   |
| <u>L16</u>            | L15 same L14   | 101                 | <u>L16</u>                   |
| <u>L15</u>            | L6 or L13  | 210504              | <u>L15</u>                   |
| <u>L14</u>            | (plurality or multiple or more than one or various or first) adj3 (hardware or graphics) adj2 (engine or accelerator or block or unit or section or module or logic or component or element) | 1657                | <u>L14</u>                   |
| <u>L13</u>            | (plurality or multiple or more than one or various or first) adj2 (digital signal proces\$4 or dsp or fpga)  | 3203                | <u>L13</u>                   |

| <u>L12</u> | L7 and (token or ring)  | 12     | <u>L12</u> |
|------------|---|--------|------------|
| <u>L11</u> | L7 and (token or ring) near3 port   | 0      | <u>L11</u> |
| <u>L10</u> | L7 and (token or ring or round) near3 port  | 0      | <u>L10</u> |
| <u>L9</u>  | L7 and (token or ring or round)   | 63     | <u>L9</u>  |
| <u>L8</u>  | L7 same (token or ring or round)  | 1      | <u>L8</u>  |
| <u>L7</u>  | L6 same L5  | 167    | <u>L7</u>  |
| <u>L6</u>  | (plurality or multiple or more than one or various or first) adj2 (\$5processor or proces\$4)                                 | 208359 | <u>L6</u>  |
| <u>L5</u>  | (plurality or multiple or more than one or various or first) adj3 ((i adj o) or (input adj output)) near3 port                | 3848   | <u>L5</u>  |
| <u>L4</u>  | L1 and L3   | 0      | <u>L4</u>  |
| <u>L3</u>  | 5226125[uref]   | 66     | <u>L3</u>  |
| <u>L2</u>  | L1 and ((i adj o) or (input adj output))  | 2      | <u>L2</u>  |
| <u>L1</u>  | (plurality or multiple or more than one or various or first) adj2 (\$5processor or proces\$4) same (ring or token) near2 port | 19     | <u>L1</u>  |

#### **END OF SEARCH HISTORY**

# **WEST Search History**

Hide Items Restore Clear Cancel

DATE: Wednesday, January 26, 2005

| Hide? | <u>Set</u><br>Name | Query  | <u>Hit</u><br>Count |
|-------|--------------------|--|---------------------|
|       | DB=I               | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ   |                     |
|       | L23                | 117 same select\$3 near9 bit   | 1                   |
|       | L22                | L21 not 120  | 23                  |
|       | L21                | 117 same select\$3   | 25                  |
|       | L19                | L17 same bit near3 register near7 select\$5  | 1                   |
|       | L17                | 14 same register file  | 107                 |
| . 🗖   | L16                | L12 and 16   | 1                   |
|       | L15                | L12 same (ownership or priority)   | 1                   |
|       | L12                | L10 same (switch\$3 or dispatch\$3 or distribut\$3 or manag\$3 or control\$4 or select\$3)   | 114                 |
|       | L9                 | 16 same control register   | 1                   |
|       | L8                 | L7 same control register   | 13                  |
|       | L7                 | 12 same 14   | 3679                |
| П     | L6                 | (plural\$5 or mulitple or more than one or various) adj3 graphics adj2 (engine or accelerator or module or unit or logic or component or device or block or element or circuit\$2 or \$5processor) | 398                 |
|       | L5                 | (plural\$5 or mulitple or more than one or various) adj3 graphics adj2 (engine or accelerator or module or unit or logic or component or device or block or element or circuit\$2 \$5processor)    | 353                 |
| n     | L4                 | graphics adj2 (engine or accelerator or module or unit or logic or component or device or block or element or circuit\$2)  | 23610               |
| П     | L3                 | (plural\$5 or mulitple or more than one or various) adj3 graphics adj2 (engine or accelerator or module or unit or logic or component or device or block or element or circuit\$2)                 | 360                 |
|       | L2                 | (dsp or digital signal proces\$4 or media proces\$4 or image proces\$4 or multiproces\$4 or parallel proces\$4 or graphics Proces\$4)  | 344618              |

## END OF SEARCH HISTORY